

**VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR**  
**A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI**  
**TABULATION OF RESULT (BACKLOG)**

**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59112 ENROLLMENT NO.: SUT17R243  
 NAME OF STUDENT: ROHIT JAISWAL FATHER'S NAME: ANIL JAISWAL

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	ABS	20		20		120	
CS352	BASIC ELECTRONICS	80	ABS	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80	28	20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80	28	20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	48	20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

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**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59115 ENROLLMENT NO.: SUT17R246  
 NAME OF STUDENT: JITENDRA SINGH FATHER'S NAME: BHALAI SINGH

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	ABS	20		20		120	
CS352	BASIC ELECTRONICS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

*Vidya*  
 Chairman of Examinations Committee

*Vidya*  
 Member Examinations Committee

*Vidya*  
 Member Examinations Committee

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**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59117 ENROLLMENT NO.: SUT17R248  
 NAME OF STUDENT: ASHISH KUMAR SONI FATHER'S NAME: MANOJ KUMAR SONI

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA	TOTAL	TOTAL	
		MAX.	OBT.	MAX.	OBT.				MAX.
CS351	MATHEMATICS-III	80	0	20		20	120		
CS352	BASIC ELECTRONICS	80	7	20		20	120		
CS353	COMPUTATIONAL SCIENCE	80	28	20		20	120		
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80	28	20		20	120		
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20	120		
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	55	20		20	120		
CS352P	BASIC ELECTRONICS LAB	40		---		20	60		
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20	60		
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20	60		
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20	60		
CS365R	VALUE EDUCATION	---		---		40	40		
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>	<b>1000</b>		
<b>RESULT: WITHHELD</b>									

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**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59312 ENROLLMENT NO.: CSE16R0004  
 NAME OF STUDENT: DIVYA SHUKLA FATHER'S NAME: SATYENDRA SHUKLA

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA	TOTAL	TOTAL	
		MAX.	OBT.	MAX.	OBT.				MAX.
CS351	MATHEMATICS-III	80	48	20		20	120		
CS352	BASIC ELECTRONICS	80		20		20	120		
CS353	COMPUTATIONAL SCIENCE	80		20		20	120		
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20	120		
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20	120		
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	57	20		20	120		
CS352P	BASIC ELECTRONICS LAB	40		---		20	60		
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20	60		
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20	60		
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20	60		
CS365R	VALUE EDUCATION	---		---		40	40		
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>	<b>1000</b>		
<b>RESULT: WITHHELD</b>									

*Vidya*  
 Chairman of Examinations Committee

*Manoj Kumar Soni*  
 Member, Evaluation Committee

*K. K. K. K.*  
 Member, Evaluation Committee

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**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59315 ENROLLMENT NO.: CSE16R0008  
 NAME OF STUDENT: JYOTI SINGH FATHER'S NAME: AJAY SINGH

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	0	20		20		120	
CS352	BASIC ELECTRONICS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	62	20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
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**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59319 ENROLLMENT NO.: CSE16R0012  
 NAME OF STUDENT: SAGAR KUMAR FATHER'S NAME: DHANNU RAM

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	BASIC ELECTRONICS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80	30	20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

*Vidya*  
 Chairman of Examinations Committee

*Vidya*  
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B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59330 ENROLLMENT NO.: CSE16R0025  
 NAME OF STUDENT: DEEPTI SHUKLA FATHER'S NAME: SATYENDRA SHUKLA

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	BASIC ELECTRONICS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80	28	20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

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**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59335 ENROLLMENT NO.: SUT17R333  
 NAME OF STUDENT: DEMANTI FATHER'S NAME: MANSIFAL

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	21	20		20		120	
CS352	BASIC ELECTRONICS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

*Vidya*  
 Chairman of Examinations Committee

*Dr. K. K. K. K.*  
 Member, Examinations Committee

*Dr. K. K. K. K.*  
 Member, Examinations Committee

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B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59339 ENROLLMENT NO.: SUT17R337  
 NAME OF STUDENT: SWATI PANDEY FATHER'S NAME: PRADEEP PANDEY

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	BASIC ELECTRONICS	80	28	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

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B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59556 ENROLLMENT NO.: CSE15R0014  
 NAME OF STUDENT: GAUTAM KUMAR FATHER'S NAME: ARVIND KUMAR

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	BASIC ELECTRONICS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80	30	20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80	47	20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

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*Handwritten signature and stamp at the bottom center.*

*Handwritten signature and stamp at the bottom right.*

**VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR**  
**A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI**  
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**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59557 ENROLLMENT NO.: CSE15R0001  
 NAME OF STUDENT: SAMBHAV KU GUPTA FATHER'S NAME: GOPAL KUMAR GUPTA

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	ABS	20		20		120	
CS352	BASIC ELECTRONICS	80	ABS	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

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**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59558 ENROLLMENT NO.: CSE15R0005  
 NAME OF STUDENT: ANANJAY DAS FATHER'S NAME: SAKHA DAS

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	BASIC ELECTRONICS	80	ABS	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80	44	20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80	41	20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80	40	20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	47	20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

*Vidya*  
 Chairman of Examinations Committee

*Vidya*  
 Member Examinations Committee

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B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59564 ENROLLMENT NO.: CSE15R0011  
 NAME OF STUDENT: PRAVEEN KUMAR PATEL FATHER'S NAME: KRIPASHANKAR PATEL

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	12	20		20		120	
CS352	BASIC ELECTRONICS	80	28	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	50	20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

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**TABULATION OF RESULT (BACKLOG)**

**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59572 ENROLLMENT NO.: CSE15R0020  
 NAME OF STUDENT: RAJKAMAL ADITYA FATHER'S NAME: MAHESH RAM ADITYA

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	BASIC ELECTRONICS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	40	20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	
<b>RESULT: WITHHELD</b>									

*VAD*  
 Chairman of Examinations Committee

*VAD*  
 Member Examinations Committee

*K. K. K.*  
 Member Examinations Committee

**VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR**  
**A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI**  
**TABULATION OF RESULT (BACKLOG)**

**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 59579 ENROLLMENT NO.: T-16/30  
 NAME OF STUDENT: UMESH KUMAR FATHER'S NAME: BALRAM

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	13	20		20		120	
CS352	BASIC ELECTRONICS	80	33	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	

**RESULT: WITHHELD**

**VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR**  
**A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI**  
**TABULATION OF RESULT (BACKLOG)**

**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 1959179 ENROLLMENT NO.: SUT18R1132  
 NAME OF STUDENT: PRATIMA LAKRA FATHER'S NAME: RAMDHARI LAKRA

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	BASIC ELECTRONICS	80	44	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	BASIC ELECTRONICS LAB	40		---		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20		60	
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20		60	
CS365R	VALUE EDUCATION	---		---		40		40	
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>		<b>1000</b>	

**RESULT: WITHHELD**

*Vidya*  
 Chairman of Examinations Committee

*Vidya*  
 Member, Evaluation Committee

*Vidya*  
 Member, Examinations Committee



**VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR**  
**A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI**  
**TABULATION OF RESULT (BACKLOG)**

**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 1959183 ENROLLMENT NO.: SUT18R1136  
 NAME OF STUDENT: RAHUL SHAKYA FATHER'S NAME: RAMESH CHANDRA SHAKYA

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA	TOTAL	TOTAL	
		MAX.	OBT.	MAX.	OBT.				MAX.
CS351	MATHEMATICS-III	80		20		20	120		
CS352	BASIC ELECTRONICS	80	30	20		20	120		
CS353	COMPUTATIONAL SCIENCE	80	40	20		20	120		
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20	120		
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20	120		
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	43	20		20	120		
CS352P	BASIC ELECTRONICS LAB	40		---		20	60		
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20	60		
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20	60		
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20	60		
CS365R	VALUE EDUCATION	---		---		40	40		
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>	<b>1000</b>		
<b>RESULT: WITHHELD</b>									

**VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR**  
**A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI**  
**TABULATION OF RESULT (BACKLOG)**

**EXAMINATION SESSION: APRIL – MAY 2020**

B.E./M.TECH. B.E. (NEW COURSE) BRANCH: COMPUTER SCIENCE ENGINEERING SEMESTER: THIRD (III)  
 ROLL NO.: 1959186 ENROLLMENT NO.: SUT18R1139  
 NAME OF STUDENT: RATNESH PRAKASH GUPTA FATHER'S NAME: YOGESH KUMAR GUPTA

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA	TOTAL	TOTAL	
		MAX.	OBT.	MAX.	OBT.				MAX.
CS351	MATHEMATICS-III	80		20		20	120		
CS352	BASIC ELECTRONICS	80	28	20		20	120		
CS353	COMPUTATIONAL SCIENCE	80		20		20	120		
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20	120		
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20	120		
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	35	20		20	120		
CS352P	BASIC ELECTRONICS LAB	40		---		20	60		
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		---		20	60		
CS356P	DIGITAL ELECTRONIC & LOGIC DESIGN LAB	40		---		20	60		
CS364P	WEB TECHNOLOGY LAB(HTML/DHTML/CSS/XML)	40		---		20	60		
CS365R	VALUE EDUCATION	---		---		40	40		
<b>TOTAL</b>		<b>640</b>		<b>120</b>		<b>240</b>	<b>1000</b>		
<b>RESULT: WITHHELD</b>									

*Vidya*  
 Chairman of Examinations Committee

*Dr. K. K. Mishra*  
 Member, Evaluation Committee

*Dr. K. K. Mishra*  
 Member, Evaluation Committee