

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: CSE16R0026

NAME OF STUDENT: MANISH SINGH

CATEGORY:

ROLL NO.: 59331

FATHER'S NAME: GURUMUKH SINGH

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	48	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80	53	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80	69	20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80	70	20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80	60	20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	48	20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40	33	--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40	31	--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40	29	--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40	30	--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: CSE15R0014

NAME OF STUDENT: GAUTAM KUMAR

CATEGORY:

ROLL NO.: 59566

FATHER'S NAME: ARVIND KUMAR

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80	63	20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80	64	20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: SUT17R262

NAME OF STUDENT: HARSH MARCUS

CATEGORY:

ROLL NO.: 59130

FATHER'S NAME: RUPESH SAMIR MARCUS

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80	62	20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	55	20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	

PERCENTAGE:

RESULT: WITHHELD

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: CSE16R0012

NAME OF STUDENT: SAGAR KUMAR

CATEGORY:

ROLL NO.: 59319

FATHER'S NAME: DHANNU RAM

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	47	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80	68	20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	48	20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	

PERCENTAGE:

RESULT: WITHHELD

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
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TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: SUT18R1139

NAME OF STUDENT: RATNESH PRAKASH GUPTA

CATEGORY:

ROLL NO.: 1959186

FATHER'S NAME: YOGESH KUMAR GUPTA

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	48	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80	49	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80	57	20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	48	20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

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EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: SUT17R249

NAME OF STUDENT: SANJEEV KUMAR

CATEGORY:

ROLL NO.: 59118

FATHER'S NAME: AJEET PAL SINGH

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	37	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80	29	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	53	20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
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TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: SUT17R252

NAME OF STUDENT: ANKIT CHOUBEY

CATEGORY:

ROLL NO.: 59121

FATHER'S NAME: VINOD CHOUBEY

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	46	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80	35	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	55	20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: CSE16R0025

NAME OF STUDENT: DEEPTI SHUKLA

CATEGORY:

ROLL NO.: 59330

FATHER'S NAME: SATYENDRA SHUKLA

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80	41	20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR

A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI

TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: CSE15R0001

NAME OF STUDENT: SAMBHAV KU GUPTA

CATEGORY:

ROLL NO.: 59557

FATHER'S NAME: GOPAL KUMAR GUPTA

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	28	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80	42	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR

A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI

TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: SUT17R248

NAME OF STUDENT: ASHISH KUMAR SONI

CATEGORY:

ROLL NO.: 59117

FATHER'S NAME: MANOJ KUMAR SONI

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	42	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80	55	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: CSE15R0005

NAME OF STUDENT: ANANJAY DAS

CATEGORY:

ROLL NO.: 59558

FATHER'S NAME: SAKHA DAS

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	28	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80	45	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: SUT17R243

NAME OF STUDENT: ROHIT JAISWAL

CATEGORY:

ROLL NO.: 59112

FATHER'S NAME: ANIL JAISWAL

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	43	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80	34	20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
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TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: CSE16R0008

NAME OF STUDENT: JYOTI SINGH

CATEGORY:

ROLL NO.: 59315

FATHER'S NAME: AJAY SINGH

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	40	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
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EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: SUT17R333

NAME OF STUDENT: DEMANTI

CATEGORY:

ROLL NO.: 59335

FATHER'S NAME: MANSIFAL

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	41	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: CSE15R0011

NAME OF STUDENT: PRAVEEN KUMAR PATEL

CATEGORY:

ROLL NO.: 59564

FATHER'S NAME: KRIPASHANKAR PATEL

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	28	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: T-16/30

NAME OF STUDENT: UMESH KUMAR

CATEGORY:

ROLL NO.: 59579

FATHER'S NAME: BALRAM

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	28	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.: EN/13/186

NAME OF STUDENT: INJMAMUL AHMAD FIRDOUSI

CATEGORY:

ROLL NO.: 59904

FATHER'S NAME: HAMJA AHMAD FIRDOUSI

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80	47	20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT: WITHHELD									

VISHWAVIDYALAYA ENGINEERING COLLEGE, LAKHANPUR
A CONSTITUENT COLLEGE OF C.S.V.T.U. BHILAI
TABULATION OF RESULT (BACKLOG)

EXAMINATION SESSION: APRIL – MAY 2020

DEGREE: B TECH

COURSE: NEW

**BRANCH: COMPUTER SCIENCE
ENGINEERING**

SEMESTER: THIRD

ENROLLMENT NO.:

NAME OF STUDENT:

CATEGORY:

ROLL NO.:

FATHER'S NAME:

GENDER:

SUBJECT CODE	NAME OF SUBJECT	ESE		CT		TA		TOTAL MAX.	TOTAL OBT.
		MAX.	OBT.	MAX.	OBT.	MAX.	OBT.		
CS351	MATHEMATICS-III	80		20		20		120	
CS352	ANALOG ELECTRONICS CIRCUITS	80		20		20		120	
CS353	COMPUTATIONAL SCIENCE	80		20		20		120	
CS354	PROBLEM SOLVING & LOGIC BUILDING USING C	80		20		20		120	
CS355	COMPUTER CONCEPTS & WEB TECHNOLOGY	80		20		20		120	
CS356	DIGITAL ELECTRONICS & LOGIC DESIGN	80		20		20		120	
CS352P	ANALOG ELECTRONICS CIRCUITS LAB	40		--		20		60	
CS354P	PROBLEM SOLVING & LOGIC BUILDING USING C LAB	40		--		20		60	
CS356P	DIGITAL ELECTRONIC AND LOGIC DESIGN LAB	40		--		20		60	
CS364P	WEB TECHNOLOGY LAB(HTML/ DHTML/CSS/XML)	40		--		20		60	
CS365R	VALUE EDUCATION	--		--		40		40	
TOTAL		640		120		240		1000	
PERCENTAGE:									
RESULT:									